

**UTTARA UNIVERSITY**  
**Semester Final Exam - Schedule**  
**Department of Computer Science & Engineering**  
**Semester: Summer– 2017 ( Day/ Regular batch)**    Version 1.4

**Wednesday 16/08/2017**

Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator
Wednesday 16/08/2017	11:00 AM - 01:00 PM	MATH-129	Complex Variable & Laplace Transforms	BSc 41A Reg	AMU	301	1,3,5	1-20	SSY+ MTR ABM+AIA
						302	1,3,5	21-45	
		CSEC 229	Computer Architecture	BSc 38A Reg	ABM	301	2,4,6	1-24	
						302	2,4,6	25-40	
		MATH-129	Complex Variable & Laplace Transforms	BSc 41B Reg	MBH	303	1,3,5	46-70	FBA+SHS MBH+ALY
						304	1,3,5	71-90	
		CSEC 229	Computer Architecture	BSc 38B Reg	MST	303	2,4,6	41-62	
						304	2,4,6	63-80	
		MATH-129	Complex Variable & Laplace Transforms	BSc 41C Reg	MBH	601	1,3,5	91-109	TYR+TSI ARM+ HGR
						602	1,3,5	110-131	
		CSEC 229	Computer Architecture	BSc 38C Reg	MAH	601	2,4,6	81-101	
						602	2,4,6	102-127	
CSEC 322	Database Management System Sessional	BSc 36A Reg	MHZ	Lab 1	ALL	ALL	MHZ+Galib		
CSEC 322	Database Management System Sessional	BSc 36B+37 Reg	SRS	LAB 2	ALL	ALL	SRS+Pervez		
ENG -100	Communication Skill	BSc 42 Reg	IWS	301	1,3,5	1-20	MTR+ALY MST+MMR		
				302	1,3,5	21-40			
CSEC 321	Database Management System	BSc 36A Reg	MHZ	301	2,4,6	1-17			
				302	2,4,6	18-35			

<b>Wednesday 16/08/2017</b>	<b>PM</b>	<b>BUS 110</b>	<b>Business Communication</b>	<b>BSc 40 Reg</b>	<b>ARI</b>	<b>303</b>	<b>1,3,5</b>	<b>1-18</b>	<b>ARI+ TSI MNH+MSM</b>
						<b>304</b>	<b>1,3,5</b>	<b>19-33</b>	
	<b>02:00 PM</b>	<b>CSEC 321</b>	<b>Database Management System</b>	<b>BSc 36B+37 Reg</b>	<b>SRS</b>	<b>303</b>	<b>2,4,6</b>	<b>36B batch</b>	
						<b>304</b>	<b>2,4,6</b>	<b>37 batch</b>	
		<b>EENG-122</b>	<b>Electrical Circuit Sessional</b>	<b>BSc 41B Reg</b>	<b>HGR</b>	<b>Lab 6</b>	<b>ALL</b>	<b>ALL</b>	<b>HGR+Shamim</b>
<b>- 04:00 PM</b>	<b>CSEC 399</b>	<b>Project I</b>	<b>BSc 32 Reg</b>	<b>MAH+CSI</b>	<b>Lab 2</b>	<b>ALL</b>	<b>ALL</b>	<b>MAH+CSI+ DMR+ SAC+SMR+Pervez</b>	
	<b>CSEC 499</b>	<b>Project II</b>	<b>BSc 31 Reg</b>	<b>FBA</b>	<b>Lab 1</b>	<b>ALL</b>	<b>ALL</b>	<b>FBA+ABM+ DMR+SAC+AIA+Galib</b>	

## Monday 21/08/2017

<b>Day</b>	<b>Time</b>	<b>Course Code</b>	<b>Course Title</b>	<b>Batch</b>	<b>Teacher</b>	<b>Room No</b>	<b>Column No</b>	<b>Student ID</b>	<b>Invigilator</b>
<b>Monday 21/08/2017</b>	<b>11:00 AM</b> <b>-</b> <b>01:00 PM</b>	<b>CSEC 213</b>	<b>Basic Electronics</b>	<b>BSc 39A Reg</b>	<b>SHC</b>	<b>301</b>	<b>1,3,5</b>	<b>1-23</b>	<b>MST+MBH TZW +MNH</b>
						<b>302</b>	<b>1,3,5</b>	<b>24-49</b>	
		<b>CSEC 325</b>	<b>Compiler Construction.</b>	<b>BSc 35A Reg</b>	<b>TZW</b>	<b>301</b>	<b>2,4,6</b>	<b>1-31</b>	<b>AMU+MTR SHS+TSI</b>
						<b>302</b>	<b>2,4,6</b>	<b>32-50</b>	
		<b>CSEC 318</b>	<b>Computer Peripheral, Interfacing &amp; Maintenance</b>	<b>BSc 34 Reg</b>	<b>DMR</b>	<b>303</b>	<b>1,3,5</b>	<b>1-31</b>	<b>AMU+MTR SHS+TSI</b>
						<b>304</b>	<b>1,3,5</b>	<b>32-74</b>	
		<b>CSEC 325</b>	<b>Compiler Construction.</b>	<b>BSc 35B Reg</b>	<b>CSI</b>	<b>303</b>	<b>2,4,6</b>	<b>51-80</b>	<b>MMR+Galib</b>
						<b>304</b>	<b>2,4,6</b>	<b>81-100</b>	
		<b>CSEC 422</b>	<b>Internet Programming &amp; Sessional(Sessional Part)</b>	<b>BSc 33 Reg</b>	<b>MMR</b>	<b>Lab 1</b>	<b>ALL</b>	<b>ALL</b>	<b>MMR+Galib</b>
		<b>CSEC 467</b>	<b>Computer Vision and Image Processing &amp; Sessional(Sessional Part)</b>	<b>BSc 32 Reg</b>	<b>ARM</b>	<b>Lab 2</b>	<b>ALL</b>	<b>ALL</b>	<b>ARM+Pervez</b>
<b>02:00 PM</b> <b>-</b> <b>04:00</b>	<b>CSEC 422</b>	<b>Internet Programming &amp; Sessional(Theory Part)</b>	<b>BSc 33 Reg</b>	<b>MMR</b>	<b>301</b>	<b>1,3,5</b>	<b>1-35</b>	<b>SSY+TYR MST+SRS</b>	
					<b>302</b>	<b>1,3,5</b>	<b>36-62</b>		
	<b>CSEC 467</b>	<b>Computer Vision and Image Processing &amp; Sessional(Theory Part)</b>	<b>BSc 32 Reg</b>	<b>ARM</b>	<b>301</b>	<b>2,4,6</b>	<b>1-27</b>		
					<b>302</b>	<b>2,4,6</b>	<b>28-56</b>		

<b>Monday 21/08/2017</b>	<b>PM</b>	<b>CSEC 326</b>	<b>Compiler Construction Sessional</b>	<b>BSc 35A Reg</b>	<b>TZW</b>	<b>LAB 1</b>	<b>ALL</b>	<b>ALL</b>	<b>TZW+Galib</b>
		<b>CSEC 326</b>	<b>Compiler Construction Sessional</b>	<b>BSc 35B Reg</b>	<b>CSI</b>	<b>LAB 2</b>	<b>ALL</b>	<b>ALL</b>	<b>CSI+SMR+Pervez</b>
	<b>02:00 PM</b>	<b>CSEC 319</b>	<b>Computer Peripheral, Interfacing &amp; Maintenance Sessional</b>	<b>BSc 34 Reg</b>	<b>DMR</b>	<b>LAB 7</b>	<b>ALL</b>	<b>ALL</b>	<b>DMR+ ARM+Rubel</b>
	<b>- 04:00 PM</b>	<b>CSEC 214</b>	<b>Electrical and Electronics Technology Sessional</b>	<b>BSc 39A Reg</b>	<b>SHC</b>	<b>LAB 6</b>	<b>ALL</b>	<b>ALL</b>	<b>SHC+Shamim</b>

## Tuesday 22/08/2017

<b>Day</b>	<b>Time</b>	<b>Course Code</b>	<b>Course Title</b>	<b>Batch</b>	<b>Teacher</b>	<b>Room No</b>	<b>Column No</b>	<b>Student ID</b>	<b>Invigilator</b>
<b>Tuesday 22/08/2017</b>	<b>11:00 AM - 01:00 PM</b>	<b>MATH-123</b>	<b>Discrete Mathematics</b>	<b>BSc 41A Reg</b>	<b>TZW</b>	<b>301</b>	<b>1,3,5</b>	<b>1-20</b>	<b>SHS+ ABM</b>
						<b>302</b>	<b>1,3,5</b>	<b>21-45</b>	<b>AIA+MNH</b>
		<b>ECON 229</b>	<b>Economics</b>	<b>BSc 38A Reg</b>	<b>MHK</b>	<b>301</b>	<b>2,4,6</b>	<b>1-24</b>	
						<b>302</b>	<b>2,4,6</b>	<b>25-40</b>	
		<b>MATH-123</b>	<b>Discrete Mathematics</b>	<b>BSc 41B Reg</b>	<b>SSY</b>	<b>303</b>	<b>1,3,5</b>	<b>46-70</b>	<b>MHZ+TYR ARM+ HGR</b>
						<b>304</b>	<b>1,3,5</b>	<b>71-90</b>	
		<b>ECON 229</b>	<b>Economics</b>	<b>BSc 38B Reg</b>	<b>MHK</b>	<b>303</b>	<b>2,4,6</b>	<b>41-62</b>	<b>MHZ+TYR ARM+ HGR</b>
						<b>304</b>	<b>2,4,6</b>	<b>63-80</b>	
		<b>MATH-123</b>	<b>Discrete Mathematics</b>	<b>BSc 41C Reg</b>	<b>SSY</b>	<b>601</b>	<b>1,3,5</b>	<b>91-109</b>	<b>JEZ+MBH MSM+ MTR</b>
						<b>602</b>	<b>1,3,5</b>	<b>110-131</b>	
		<b>ECON 229</b>	<b>Economics</b>	<b>BSc 38C Reg</b>	<b>JEZ</b>	<b>601</b>	<b>2,4,6</b>	<b>81-101</b>	<b>JEZ+MBH MSM+ MTR</b>
						<b>602</b>	<b>2,4,6</b>	<b>102-127</b>	
		<b>PHY 111</b>	<b>Modern Physics Sessional</b>	<b>BSc 40 Reg</b>	<b>AAM</b>	<b>Lab 5</b>	<b>ALL</b>	<b>ALL</b>	<b>AAM+Shamim</b>
<b>02:00 PM - 04:00</b>	<b>STAT -121</b>	<b>Statistics &amp; Probability</b>	<b>BSc 42 Reg</b>	<b>MTR</b>	<b>301</b>	<b>1,3,5</b>	<b>1-20</b>	<b>SHS+AMU TSI+ARM</b>	
					<b>302</b>	<b>1,3,5</b>	<b>21-40</b>		
	<b>CSEC 324</b>	<b>Measurement &amp; Instrumentation</b>	<b>BSc 36A Reg</b>	<b>SHK</b>	<b>301</b>	<b>2,4,6</b>	<b>1-17</b>	<b>SHS+AMU TSI+ARM</b>	
					<b>302</b>	<b>2,4,6</b>	<b>18-35</b>		
<b>PHY 110</b>	<b>Modern Physics</b>	<b>BSc 40 Reg</b>	<b>AAM</b>	<b>303</b>	<b>1,3,5</b>	<b>1-18</b>	<b>AAM+ MHZ</b>		

	PM				304	1,3,5	19-33	MTR+ABM	
		CSEC 324	Measurement & Instrumentation	BSc 36B+37 Reg	SHK	303	2,4,6		36B batch
						304	2,4,6		37 batch
		EENG-122	Electrical Circuit Sessional	BSc 41C Reg	SHC	Lab 6	ALL	SHC+Shamim	

### Wednesday 23/08/2017

Day	Time	Course	Course Title	Batch	Teacher	Room	Column	Student ID	Invigilator		
Wednesday 23/08/2017	11:00 AM - 01:00 PM	CSEC 215	Data Structure	BSc 39A Reg	MSH	301	1,3,5	1-23	MHZ+SHC AMU+MSM		
						302	1,3,5	24-49			
		CSEC 311	Design & Analysis of Algorithm	BSc 35B Reg	MAH	301	2,4,6	1-31			
						302	2,4,6	32-50			
		CSEC 422	Internet Programming & Sessional(Theory Part)	BSc 32 Reg	FBA	303	1,3,5	1-27	SHS+TYR SRS+ ABM		
						304	1,3,5	28-56			
			CSEC 311	Design & Analysis of Algorithm	BSc 35A Reg	SMR	303	2,4,6	51-80		
							304	2,4,6	81-100		
			CSEC 422	Internet Programming & Sessional(Sessional Part)	BSc 34 Reg	MST	Lab 1	ALL	ALL	MST+Galib	
	02:00 PM - 04:00 PM		CSEC 412	Management Information System	BSc 33 Reg	SHK	301	1,3,5	1-35	TSI+MBH MNH+ ARM	
							302	1,3,5	36-62		
				CSEC 422	Internet Programming & Sessional(Theory Part)	BSc 34 Reg	MST	301	2,4,6	1-31	
								302	2,4,6	32-74	
				CSEC 312	Design & Analysis of Algorithm Sessional	BSc 35B Reg	MAH	LAB 1	ALL	ALL	MAH+Galib
			CSEC 312	Design & Analysis of Algorithm Sessional	BSc 35A Reg	SMR	LAB 2	ALL	ALL	SMR+Pervez	
			CSEC 422	Internet Programming & Sessional(Sessional Part)	BSc 32 Reg	FBA	LAB 7	ALL	ALL	FBA+Rubel	
		CSEC 216	Data Structure Sessional	BSc 39A Reg	MSH	LAB 8	ALL	ALL	MSH+Shamim		

### Thursday 24/08/2017

Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator	
Thursday 24/08/2017	11:00 AM - 01:00 PM	CSEC -121	Programming Language & Application-I	BSc 41A Reg	SHS	301	1,3,5	1-20	MST+TZW MBH+AMU	
						302	1,3,5	21-45		
		CSEC -111	Computer System & Application	BSc 42 Reg	MSM	301	2,4,6	1-20		
						302	2,4,6	21-40		
	Thursday 24/08/2017	11:00 AM - 01:00 PM	CSEC -121	Programming Language & Application-I	BSc 41B Reg	TSI	303	1,3,5	46-70	ABQ+SHC MSM+SRS
							304	1,3,5	71-90	
			ACTG 121	Accounting	BSc 40 Reg	ABQ	303	2,4,6	1-18	
							304	2,4,6	19-33	
			CSEC -121	Programming Language & Application-I	BSc 41C Reg	MMR	601	1,3,5	91-109	
							602	1,3,5	110-131	
CSEC 226		Microprocessor Interfacing & Low Level Language Sessional	BSc 38A Reg	KTA	LAB 1	ALL	ALL	KTA+Galib		
CSEC 226		Microprocessor Interfacing & Low Level Language Sessional	BSc 38B Reg	MNH	LAB 2	ALL	ALL	MNH+Pervez		
CSEC 226		Microprocessor Interfacing & Low Level Language Sessional	BSc 38C Reg	HGR	LAB 7	ALL	ALL	HGR+Rubel		
Thursday 24/08/2017		02:00 PM - 04:00 PM	CSEC 225	Microprocessor Interfacing & Low Level Language	BSc 38A Reg	KTA	301	1,3,5	1-24	KTA+SHC SRS+ABM
	302						1,3,5	25-40		
	CSEC 323		Data Communication	BSc 36A Reg	ALY	301	2,4,6	1-17		
						302	2,4,6	18-35		
	CSEC 225		Microprocessor Interfacing & Low Level Language	BSc 38B Reg	MNH	303	1,3,5	41-62	MST+SMR AMU+ ALY	
						304	1,3,5	63-80		
	CSEC 323		Data Communication	BSc 36B+37 Reg	ALD	303	2,4,6	36B batch		
						304	2,4,6	37 batch		
	CSEC 225		Microprocessor Interfacing & Low Level Language	BSc 38C Reg	HGR	601	1,3,5	81-101	TZW+SSY	
						602	1,3,5	102-127		
CSEC -112	Computer System & Application Sessional	BSc 42 Reg	MSM	Lab 1	ALL	ALL	MSM+Galib			

		CSEC -122	Programming Language & Application-I Sessional	BSc 41A Reg	SHS	Lab 2	ALL	ALL	SHS+Pervez
		CSEC -122	Programming Language & Application-I Sessional	BSc 41B Reg	TSI	Lab 7	ALL	ALL	TSI+Rubel
		CSEC -122	Programming Language & Application-I Sessional	BSc 41C Reg	MMR	Lab 8	ALL	ALL	MMR+ Tariqul Islam

## Friday 25/08/2017

Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator
Friday 25/08/2017	10:00 AM - 12:00 PM	CSEC 217	Digital Logic Design	BSc 39A Reg	SSY	301	1,3,5	1-23	MMR+MHZ MTR+FBA
						302	1,3,5	24-49	
		CSEC 428	Computer Graphics	BSc 35A Reg	MSH	301	2,4,6	1-31	
						302	2,4,6	32-50	
		CSEC 432	Computer Communication & Networks	BSc 31 Reg	ALD	303	1,3,5		SMR+MNH ARM+MST
						304	1,3,5		
	CSEC 428	Computer Graphics	BSc 35B Reg	ALY	303	2,4,6	51-80		
					304	2,4,6	81-100		
	CSEC 424	Artificial Intelligence and Expert Systems & Sessional(Sessional Part)	BSc 33 Reg	TYR	Lab 1	ALL	ALL	ALL	TYR+Galib
	CSEC 431	Software Engineering Sessional	BSc 34 Reg	SAC	Lab 2	ALL	ALL	ALL	SAC+ALY+Pervez
	02:30 PM - 04:30 PM	CSEC 424	Artificial Intelligence and Expert Systems & Sessional(Theory Part)	BSc 33 Reg	TYR	301	1,3,5	1-35	HGR+TZW MMR+ MSM
						302	1,3,5	36-62	
CSEC 430		Software Engineering	BSc 34 Reg	SAC	301	2,4,6	1-31		
					302	2,4,6	32-74		
CSEC 429	Computer Graphics Sessional	BSc 35A Reg	MSH	LAB 1	ALL	ALL	ALL	MSH+Tariqul Islam	
CSEC 429	Computer Graphics Sessional	BSc 35B Reg	ALY	LAB 2	ALL	ALL	ALL	ALY+Pervez	

		CSEC 433	Computer Communication & Networks Sessional	BSc 31 Reg	ALD	LAB 7	ALL	ALL	ALD+Rubel
		CSEC 218	Digital Logic Design Sessional	BSc 39A Reg	SSY	LAB 3	ALL	ALL	SSY+Kaisar

## Saturday 26/08/2017

Day	Time	Course Code	Course Title	Batch	Teacher	Room No	Column No	Student ID	Invigilator
Saturday 26/08/2017	11:30 AM - 01:30 PM	EENG-121	Electrical Circuit	BSc 41A Reg	MNH	301	1,3,5	1-20	MNH+AMU SHS+TSI
						302	1,3,5	21-45	
		MATH 221	Linear Algebra and Fourier Transformation	BSc 38A Reg	AMU	301	2,4,6	1-24	
						302	2,4,6	25-40	
		EENG-121	Electrical Circuit	BSc 41B Reg	HGR	303	1,3,5	46-70	TZW+MBH MSM+MST
						304	1,3,5	71-90	
		MATH 221	Linear Algebra and Fourier Transformation	BSc 38B Reg	AMU	303	2,4,6	41-62	
						304	2,4,6	63-80	
		EENG-121	Electrical Circuit	BSc 41C Reg	SHC	601	1,3,5	91-109	SMR+SHC FBA+ MHZ
						602	1,3,5	110-131	
		MATH 221	Linear Algebra and Fourier Transformation	BSc 38C Reg	MBH	601	2,4,6	81-101	
						602	2,4,6	102-127	
		CSEC -212	Programming Language & Application-II Sessional	BSc 40 Reg	MTR	Lab 1	ALL	ALL	MTR+Galib
		CSEC 317	System Analysis and Design Sessional	BSc 36A Reg	ABM	Lab 2	ALL	ALL	ABM+Tariqul Islam
CSEC 317	System Analysis and Design Sessional	BSc 36B+37 Reg	AIA	LAB 7	ALL	ALL	AIA+Rubel		
02:30 PM -	MATH-110	Differential & Integral Calculus	BSc 42 Reg	MBH	301	1,3,5	1-20	SSY+AIA ALY+ARM	
					302	1,3,5	21-40		
	CSEC 316	System Analysis and Design	BSc 36A Reg	ABM	301	2,4,6	1-17		
					302	2,4,6	18-35		

	<b>04:30 PM</b>	<b>CSEC -211</b>	<b>Programming Language &amp; Application-II</b>	<b>BSc 40 Reg</b>	<b>MTR</b>	<b>303</b>	<b>1,3,5</b>	<b>1-18</b>	<b>MHZ+SRS</b>
						<b>304</b>	<b>1,3,5</b>	<b>19-33</b>	
		<b>CSEC 316</b>	<b>System Analysis and Design</b>	<b>BSc 36B+37 Reg</b>	<b>AIA</b>	<b>303</b>	<b>2,4,6</b>	<b>36B batch</b>	
						<b>304</b>	<b>2,4,6</b>	<b>37 batch</b>	
		<b>EENG-122</b>	<b>Electrical Circuit Sessional</b>	<b>BSc 41A Reg</b>	<b>MNH</b>	<b>Lab 6</b>	<b>ALL</b>	<b>ALL</b>	<b>MNH+Shamim</b>